

MODEL 2032SP SERIAL TO PARALLEL CARD

The Model 2032SP Serial to Parallel card is a component of the IED Series 2000 UDAPS™. Its purpose is to take up to 32 serial digital signals from up to four 2008AD cards and place these signals on the internal parallel digital buses in the proper time slots. The time slot assignment is part of the time-division multiplexing scheme used in the UDAPS™. The slot assignments are defined in the UDAPS™ configuration information which is downloaded from the 590 Microcomputer.

The 2032SP is designed to be mounted in the 2022M Mainframe, through which its power and signal connections are made. More than one card may be used in a system. The 2032SP card provides one of the digital interfaces between the Models 2012 and 2022 Mainframes in a UDAPS™ system.

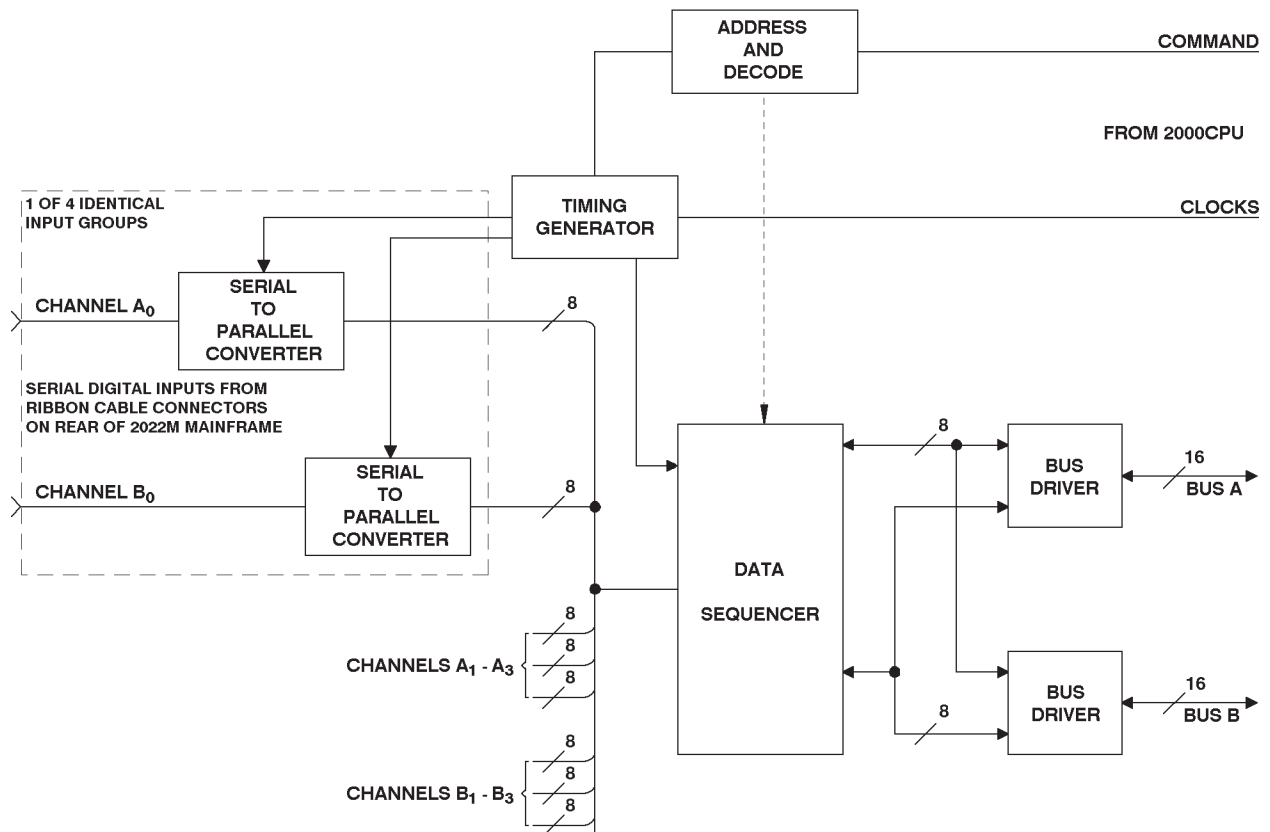


Figure 1 - 2032SP Block Diagram



SPECIFICATIONS

ELECTRICAL, ANALOG

- 1. Power Supply
 - Supply Voltage Range
 - +5 V supply. 4.75 V - 5.25 V
 - Supply Current (all relays energized)
 - V= +5 V. 75 mA

INDICATORS

- 1. Card Accessed. Green LED

CONNECTORS

- 1. 100-pin Euro connector (male) Hirose PCN13-100S-2.54DS
For card connector pin connections see Table , page .

MECHANICAL

- 1. Size (maximum overall dimensions as viewed from front)
 - Height (16.3 cm) 6.42"
 - Width. (1.96 cm) 0.77"
 - Depth (31.9 cm) 12.55"
 - Weight (315 gm) 0.696 lb

ENVIRONMENTAL

- 1. Operating Temperature Range (+32 °F - +104 °F) 0 °C - +40 °C
- 2. Storage Temperature Range (-40 °F - +158 °F) -40 °C - +70 °C

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PIN	FUNCTION	PIN	FUNCTION
1	Serial Data Channels 5-8	51	Serial Data Channels 1-4
2	Spare 2	52	Spare 1
3	Serial Data Channels 13-16	53	Serial Data Channels 9-12
4	Spare 7	54	Spare 4
5	Spare 6	55	Buffered Serial Clock Out
6	Spare 3	56	Spare 5
7	Serial Data Channels 21-24	57	Serial Data Channels 17-20
8	Buffered Serial Sync Out	58	Spare 8
9	Serial Data Channels 25-28	59	Spare 9
10	Spare 10	60	Serial Data Channels 29-32
11	Spare 12	61	Spare 11
12	Spare 14	62	Spare 13
13	Board Select 0*	63	Board Select 1*
14	Board Select 2*	64	Board Select 3*
15	Board Select 4*	65	Ground
16	Serial Clock	66	Serial Sync
17	Control Address Strobe (Inverted)	67	Control Write (Inverted)
18	Swap	68	Disable (Inverted)
19	Parallel Clock	69	Parallel Sync
20	Control Acknowledge (Inverted)	70	Spare Bus
21	Control Data 6	71	Control Data 7
22	Control Data 4	72	Control Data 5
23	Control Data 2	73	Control Data 3
24	Control Data 0	74	Control Data 1
25	Control Address 14	75	Control Address 15
26	Control Address 12	76	Control Address 13
27	Control Address 10	77	Control Address 11
28	Control Address 8	78	Control Address 9
29	Control Address 6	79	Control Address 7
30	Control Address 4	80	Control Address 5
31	Control Address 2	81	Control Address 3
32	Control Address 0	82	Control Address 1
33	Digital Audio Data Bus B14	83	Digital Audio Data Bus B15
34	Digital Audio Data Bus B12	84	Digital Audio Data Bus B13
35	Digital Audio Data Bus B10	85	Digital Audio Data Bus B11
36	Digital Audio Data Bus B8	86	Digital Audio Data Bus B9
37	Digital Audio Data Bus B6	87	Digital Audio Data Bus B7
38	Digital Audio Data Bus B4	88	Digital Audio Data Bus B5
39	Digital Audio Data Bus B2	89	Digital Audio Data Bus B3

Table 1 - 2032PS Card Connector Pin Connections



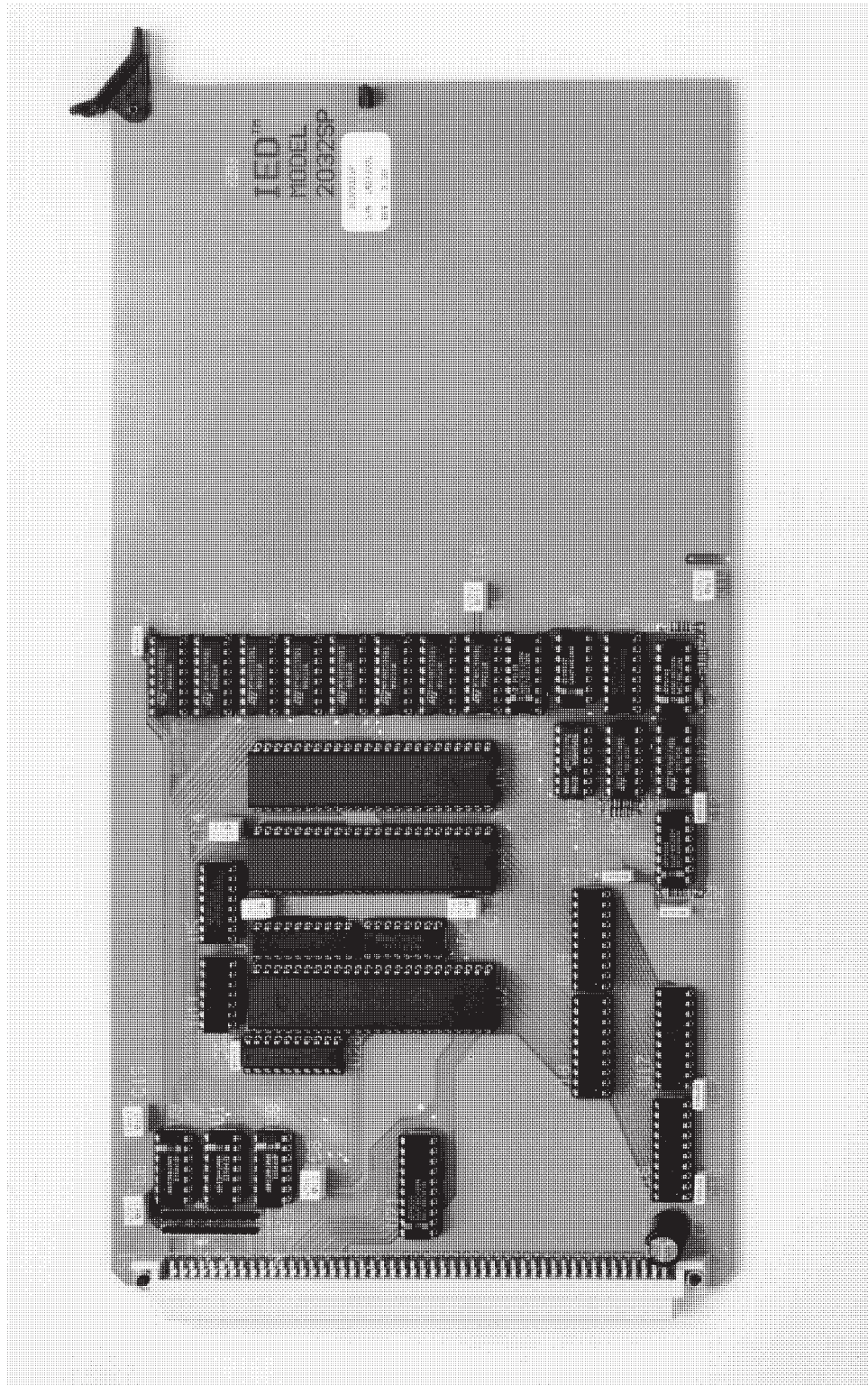


Figure 2 - 2032SP Serial to Parallel Card

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